

New title

PATENT

Customer No. 22,852

# 8B Andl.

Attorney Docket No. 04329.2613

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Katsuhiko HIEDA

Application No.: 09/916,509

Filed: July 30, 2001

For:

FIELD EFFECT TRANSISTOR

USING SIDE-WALL GATES TO PREVENT PUNCH-THROUGH

(As Amended Herein)

Commissioner for Patents Washington, DC 20231

Sir:

Group Art Unit: T. Le

Examiner: 2814

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NIS 21 2002
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TECHNOLOGY CENTER 280

## **AMENDMENT**

In reply to the Office Action dated May 17, 2002, the period for response to which extends through August 19, 2002 (with August 17, 2002 being a Saturday), please amend the application as follows:

## IN THE TITLE:

Please change the title to read - FIELD EFFECT TRANSISTOR USING SIDE-

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WALL GATES TO PREVENT PUNCH-THROUGH--.

FINNEGAN HENDERSON FARABOW GARRETT& DUNNER LLP

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## IN THE SPECIFICATION:

Please amend the specification as follows: